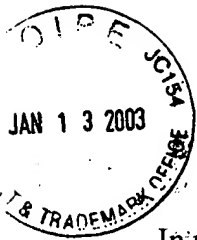


2815



RECEIVED  
PATENT

JAN 14 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

TECHNOLOGY CENTER 2800

In re Application of:

MANFRED HORSTMANN  
ROLF STEPHAN  
KARSTEN WIECZOREK  
STEPHEN KRUEGEL

Examiner: J. Fenty

Group Art Unit: 2815

Att'y Docket: 2000.064200

*A/C*

*1/23/03*

*Smith*

Serial No.: 09/847,622

Filed: May 2, 2001

For: FIELD EFFECT TRANSISTOR WITH  
REDUCED GATE DELAY AND  
METHOD OF FABRICATING THE  
SAME

**RESPONSE TO OFFICE ACTION DATED OCTOBER 23, 2002**

CERTIFICATE OF MAILING 37 C.F.R. 1.8	
I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on the date below:	
<u>01-07-03</u> Date	<u>Kathy Moras</u> Signature

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

This paper is in response to the Office Action dated October 23, 2002, for which the three-month date for response is January 23, 2003.

It is believed that no fee is required in connection with this response. However, the Assistant Commissioner is authorized to **deduct any fees required under 37 C.F.R. §§ 1.16 to 1.21 from the Advanced Micro Devices, Inc. Deposit Account No. 01-0365/DE0053.** In the event the monies in that account are insufficient, the Assistant Commissioner is authorized to